



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Art Unit : 2814

Examiner: Mr. Phat X. Cao

#12
12/6/01
Jules

PATENT APPLICATION of

Applicants : Yasutaka SAKAINO, et al.

Appln. No.: 09/229,628

Filed : January 13, 1999

For : SEMICONDUCTOR INTEGRATED
CIRCUIT

Atty. Dkt. :
32014-141666 (Formerly OKI 4646.01)

**FURTHER PROPOSED
DRAWING CHANGES**

November 29, 2001

Customer No.



26694

PATENT TRADEMARK OFFICE

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Approval of the proposed drawing change, marked in red ink on the copy of Figure 5 attached hereto, is respectfully requested. The presently proposed change for Figure 5 supersedes the change proposed on February 13, 2001.

Respectfully submitted,

Allen Wood

Allen Wood

Registration No. 28,134

VENABLE

P.O. Box 34385

Washington, D.C. 20043-9998

Telephone: (202) 962-4800

Telefax : (202) 962-8300